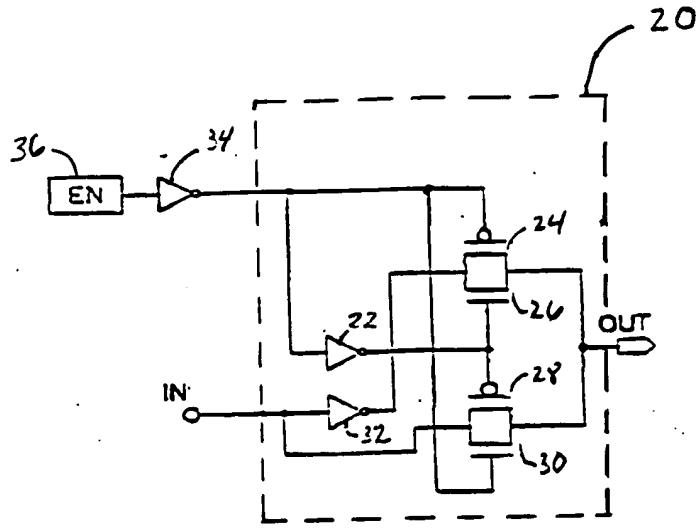

20
36
34
EN
24
26
22
OUT
28
IN
32
30

FIG. 1

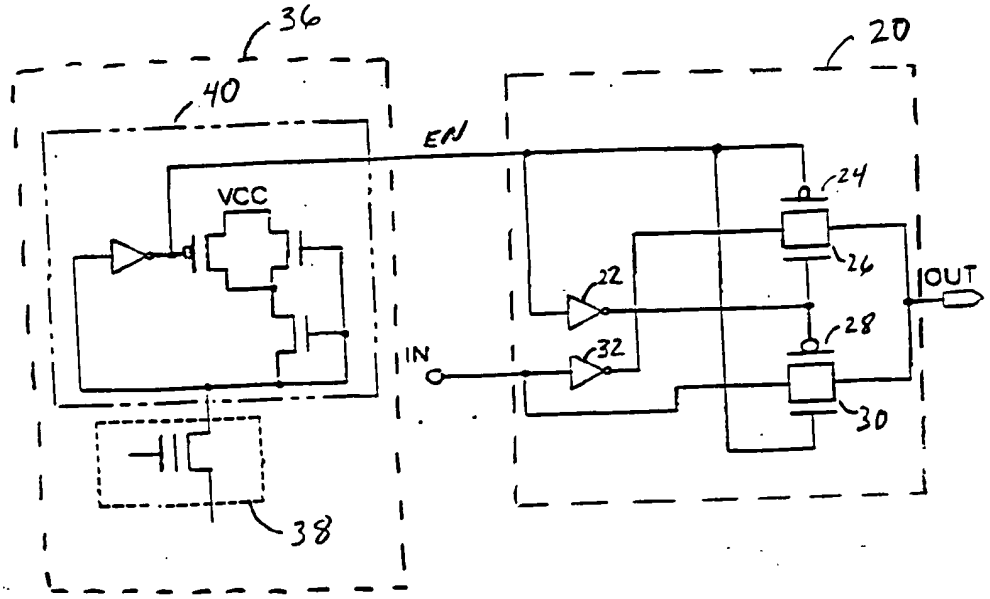

36
40
20
EN
VCC
24
26
22
OUT
28
IN
32
30
38

FIG. 2

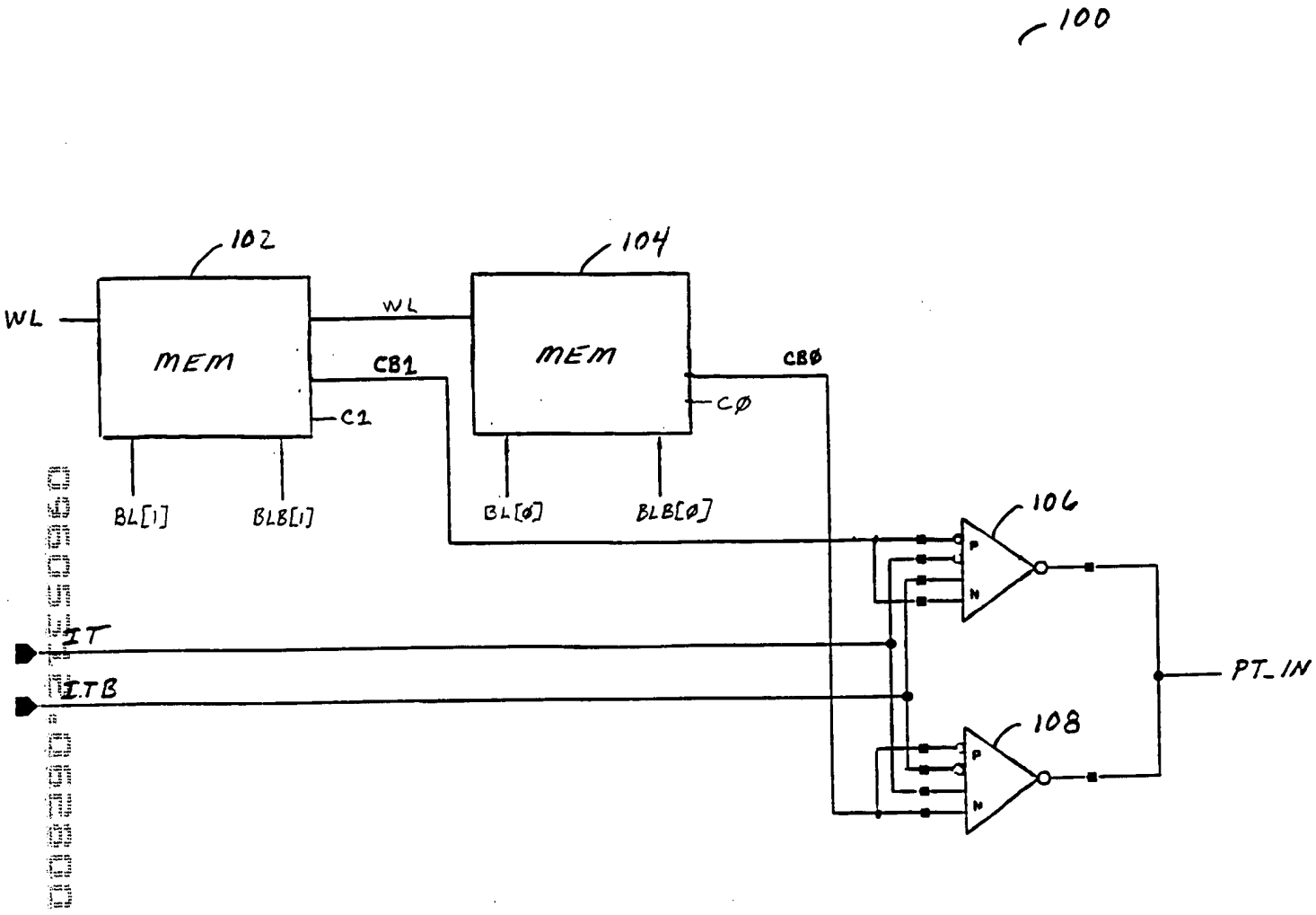

100
102
104
WL
MEM
WL
MEM
CB1
CB0
C1
C0
BL[1]
BLB[1]
BL[0]
BLB[0]
106
P
N
IT
ITB
PT_IN
108
P
N

FIG. 3

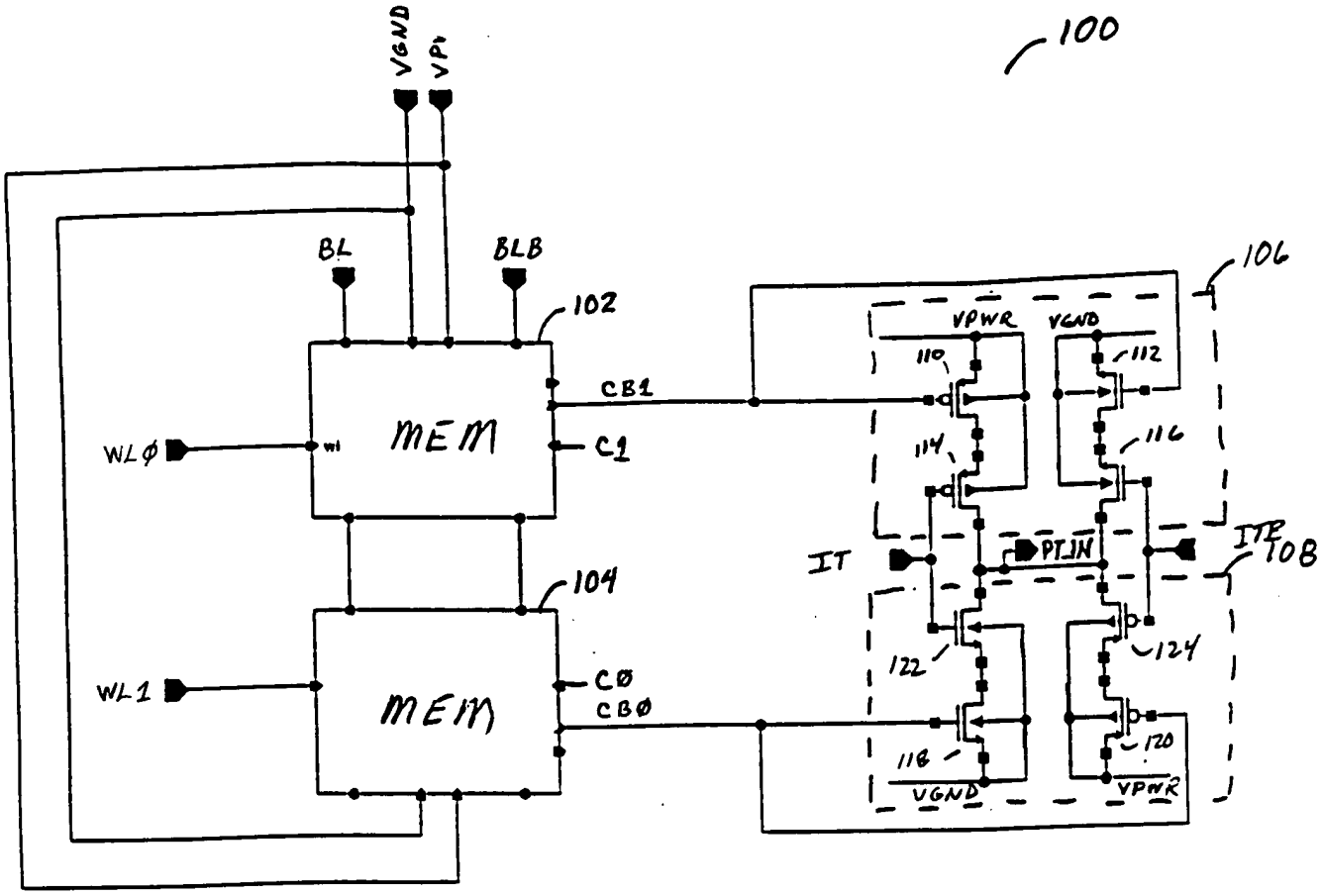

100
VGND
VPWR
BL
BLB
102
106
MEM
WL0
WL1
CB1
C1
C0
CB0
104
VPWR
VGND
110
112
114
116
IT
PT.IN
ITB
122
124
118
120
VGND
VPWR

FIG. 4

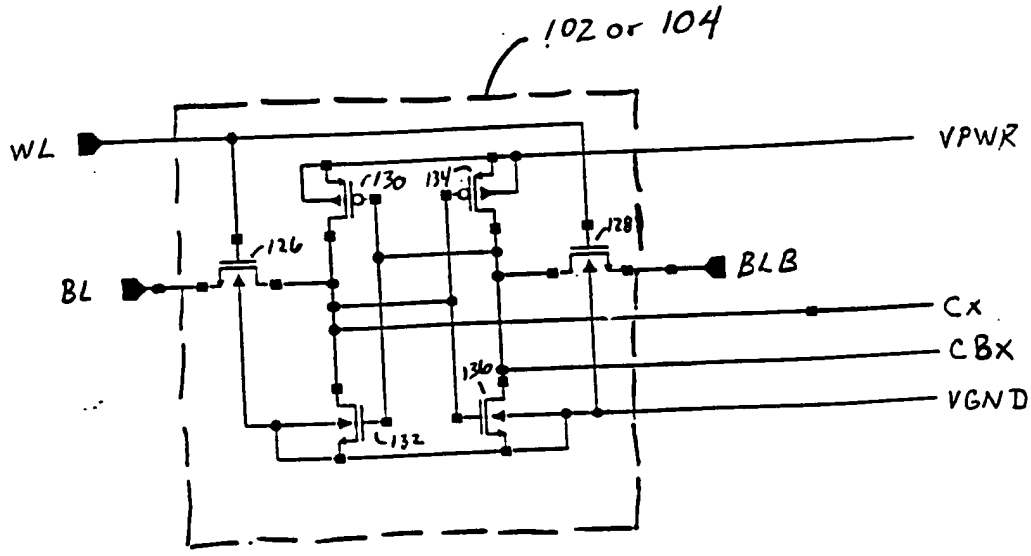

102 or 104
WL
VPWR
130
134
126
128
BL
BLB
Cx
CBx
136
VGND
132

FIG. 5

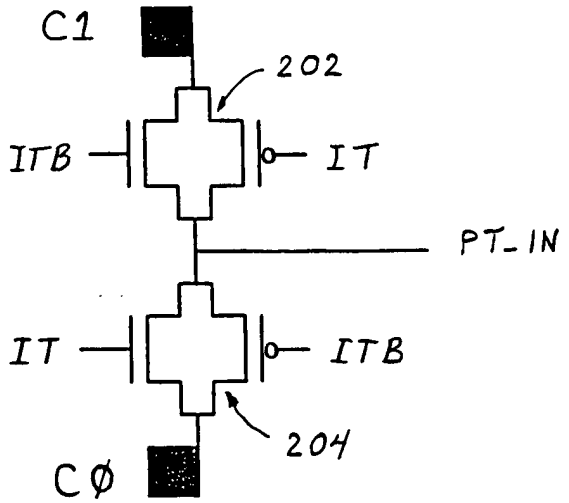

C1
202
ITB
IT
PT_IN
IT
ITB
204
CØ

FIG. 6

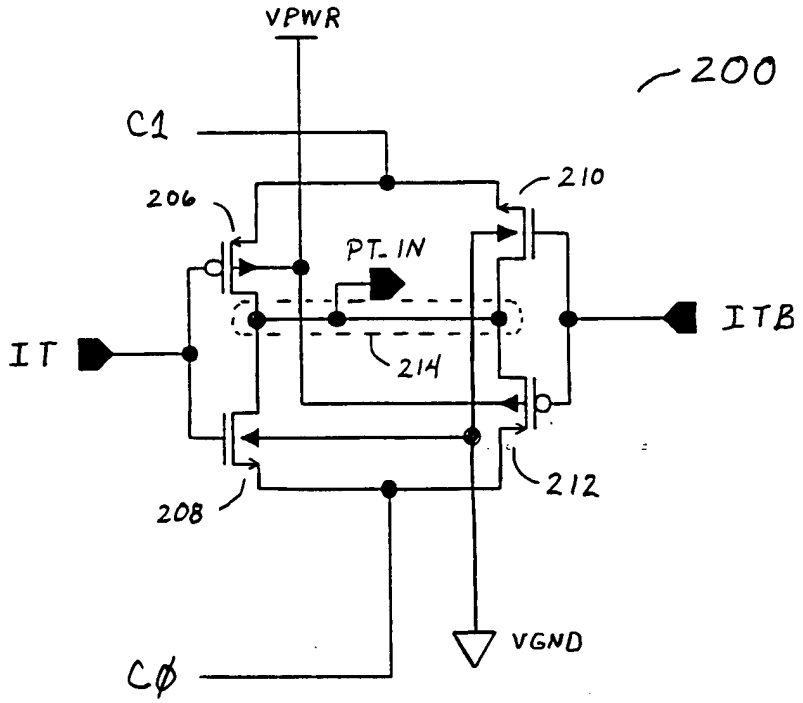

VPWR
200
C1
210
206
PT_IN
ITB
IT
214
212
208
VGND
CØ

FIG. 7

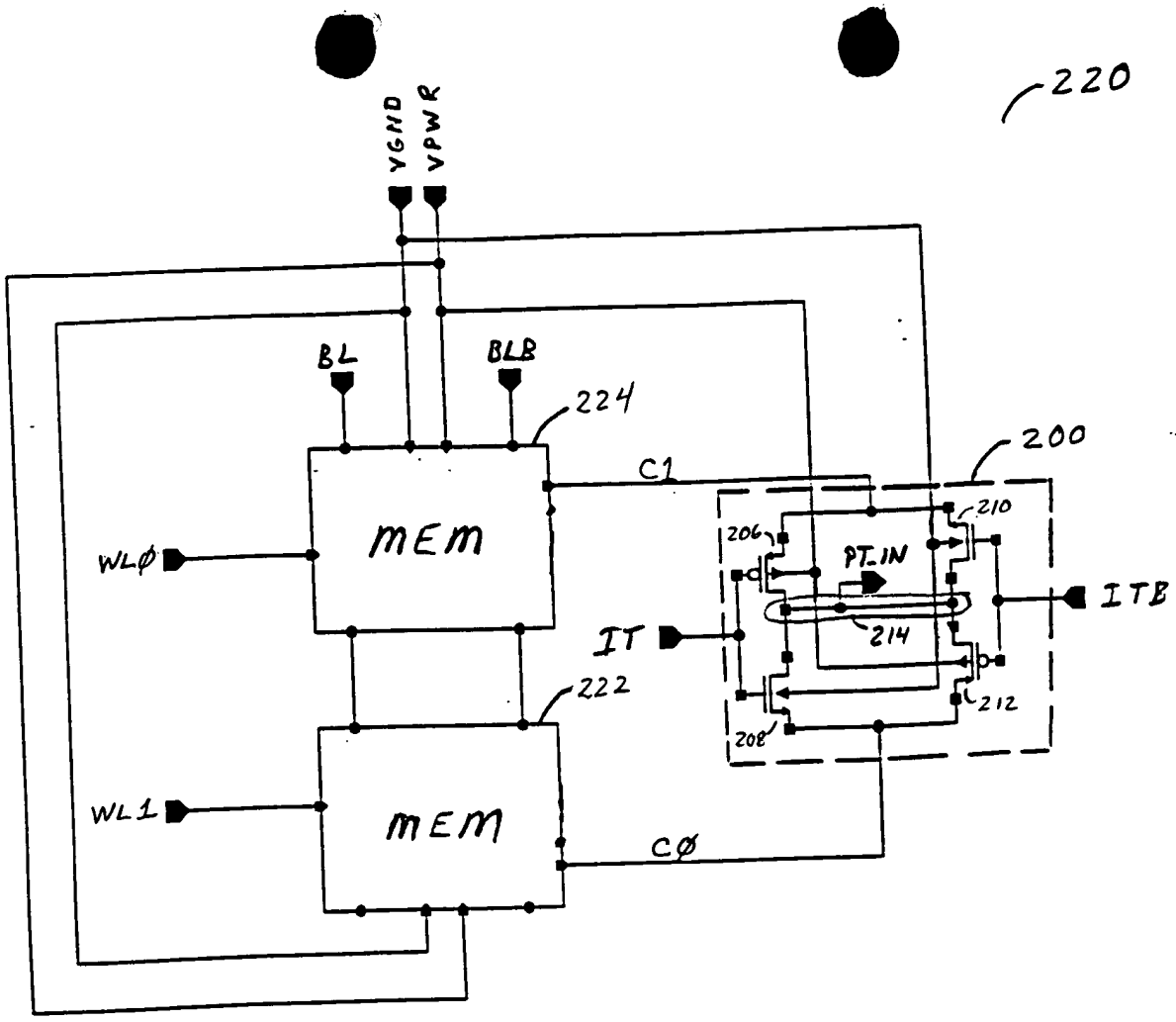

220
VGND
VPWR
BL
BLB
224
200
C1
MEM
WLØ
206
210
PT_IN
ITB
IT
214
222
212
208
WL1
MEM
CØ

FIG. 8

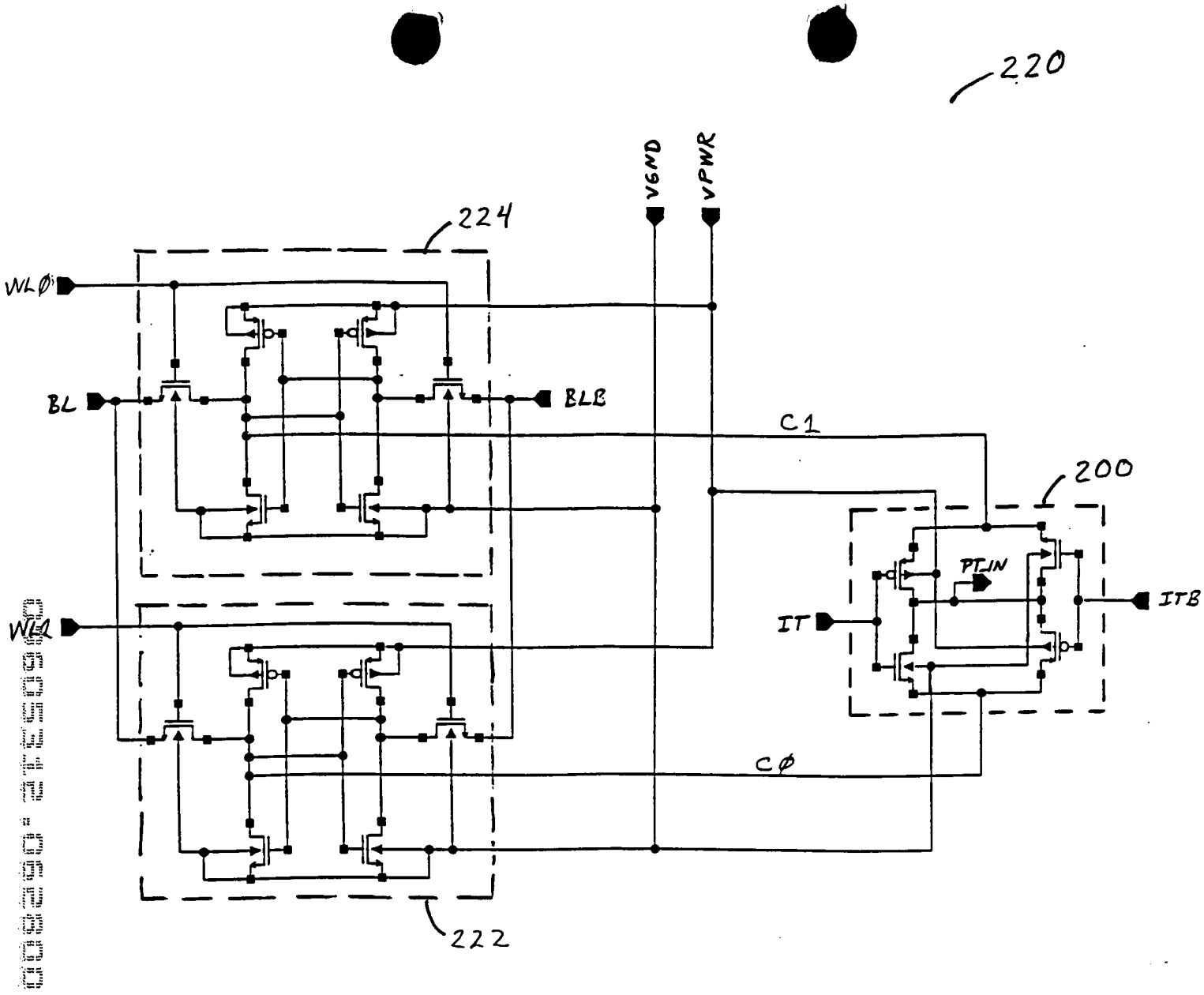



FIG. 9

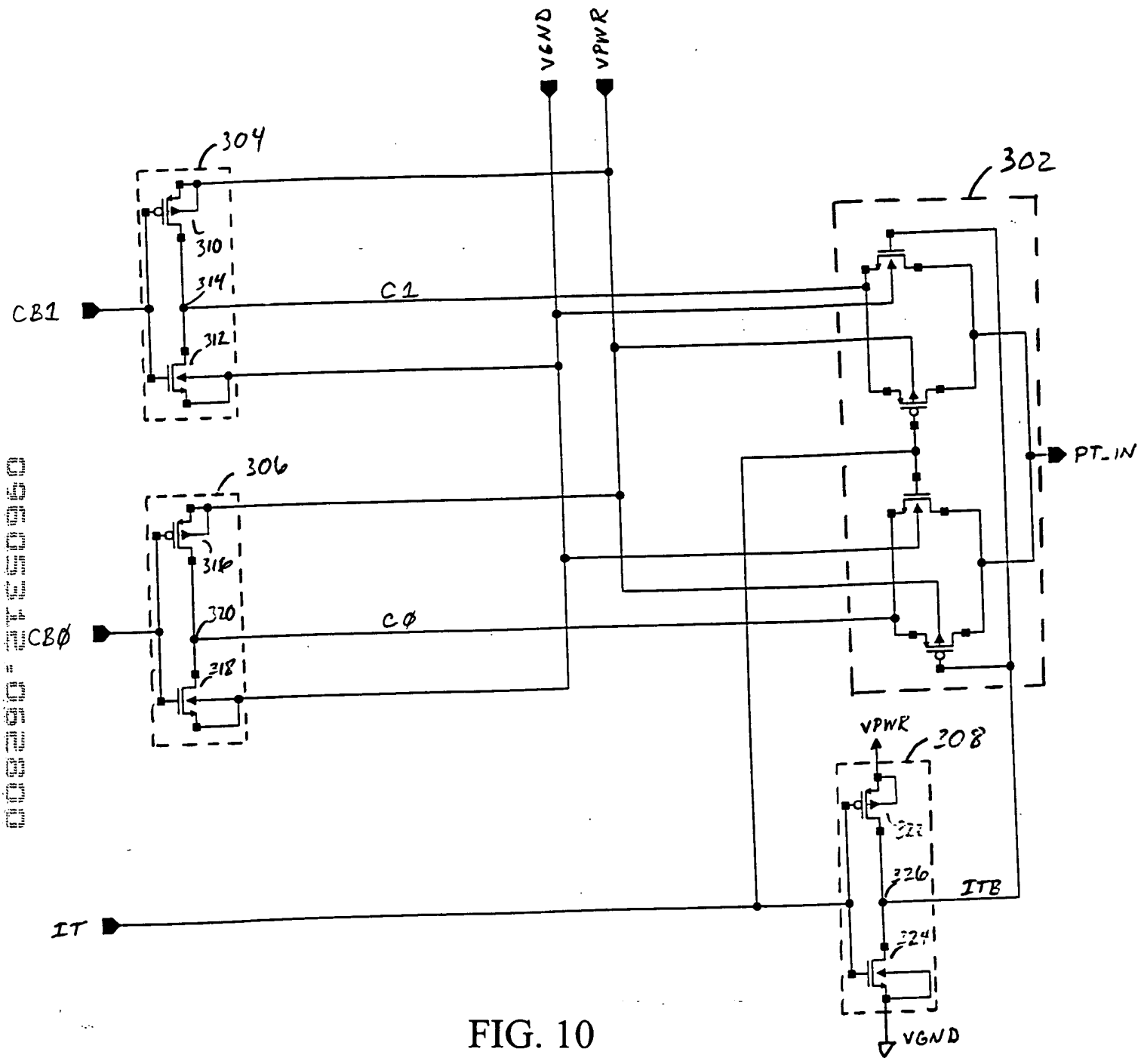

300
VGND
VPWR
304
310
314
312
CB1
C1
302
PT_IN
306
316
320
318
CBØ
CØ
VPWR
308
322
326
ITB
324
IT
VGND

FIG. 10